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54 Mos integrated circuit for driving light-emitting diodes.

57 An MOS integrated circuit for driving light-emitting diodes (LED's) comprises first constant current circuits, each of which is for one of the LED's and includes a first drive circuit for maintaining the drive current for the associated LED at a predetermined value established by a first reference voltage. The integrated circuit further includes a second constant current circuit which is connected to all of the first constant current circuits to form a mirror circuit with each of said first constant current circuits. A current detecting element detects a predetermined magnitude of current flowing therethrough and develops a current-representative detection voltage. A second drive circuit within the second constant current circuit responds to the detection voltage and a second reference voltage by maintaining said first reference voltage at a predetermined value so that the LED drive current is determined by the said detection voltage and second reference voltage.

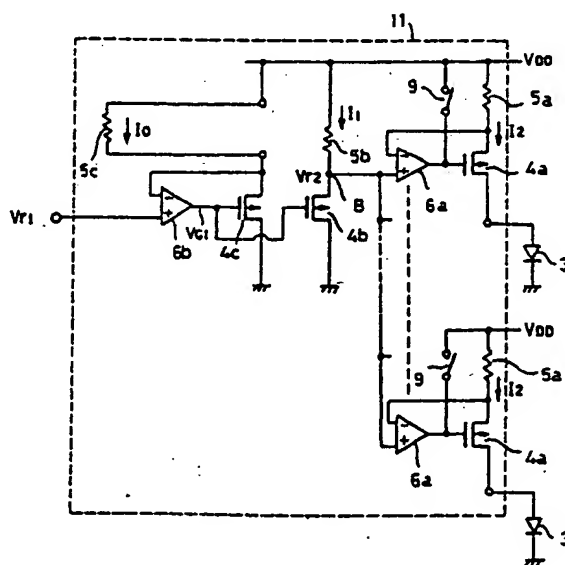


FIG. 2

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MOS INTEGRATED CIRCUIT FOR DRIVING LIGHT-EMITTING DIODES

This invention relates to an MOS integrated circuit for driving light-emitting diodes (LED's), included, for example, in an LED array in a head which is used as a light source in an electrophotographic printer.

Usually, an LED array head includes several thousands of LED's, and, in order to drive these LED's, several tens to several thousands of integrated circuits each having 32 to 64 driver circuits must be used.

Conventionally, as LED driving integrated circuits, MOS integrated circuits shown, for example, in the official gazette of Unexamined Japanese Patent Publication No. SHO 60-198872 have been used.

Figure 1 shows an LED driving MOS integrated circuit disclosed in the above-stated Unexamined Japanese Patent Publication No. SHO-60-198872.

Referring to Figure 1, an LED driving circuit 1 controls power supplied to LED's 3a-3c from a DC power supply 2 so that the brightness of the LED's is maintained at a predetermined value. The LED driving circuit 1 is fabricated as an MOS integrated circuit on a single substrate.

The LED driving MOS integrated circuit 1 comprises MOS transistors 4 connected in series with the respective LED's 3a-3c. The MOS transistors 4 serve as current limiting elements for limiting the values of currents flowing through the respective LED's 3a-3c. The MOS transistors 4 are connected to the positive terminal of the DC supply 2 through associated resistors 5. A desired LED driving current limited by the MOS transistors 4 flows through the associated resistors 5, and a voltage V_1 at the level determined by the magnitude of current flowing through the respective ones of the LED's 3a-3c is developed across the resistor 5. The voltage V_1 is applied to a + input terminal of an associated voltage comparator 6.

A depletion-type MOS transistor 7 having its gate electrode connected directly to its own source electrode is used to derive a reference voltage V_r which is at a constant level regardless of the level of the terminal voltage V_0 of the power supply 2 and is applied to - input terminals of the respective voltage comparators 6. The drain electrode of the transistor 7 is connected to the - input terminals of the respective voltage comparators 6 and also connected through a resistor 8 to the positive terminal of the power supply 2. The source electrode of the transistor 7 is grounded together with its gate electrode.

The drain current I_D of the transistor 7 flowing through the resistor 8 causes the voltage V_r to be developed across the resistor 8, and this voltage is

applied as a reference voltage to the - input terminals of the voltage comparators 6.

Thus, each of the voltage comparators 6 develops an output voltage V_G in accordance with the difference between the voltage V_1 and the reference voltage V_r . This output voltage V_G controls the conduction of the associated transistor 4. When $V_1 < V_r$, the output voltage V_G of each voltage comparator 6 increases in the positive direction to increase the drain current of the transistor 4, which in turn causes the voltage drop across the associated resistor 5 to increase. When $V_1 > V_r$, the output voltage V_G of each voltage comparator 6 increases in the negative direction so that the drain current of the transistor 4 decreases, which in turn causes decrease in voltage drop across the resistor 5. Thus, the voltage V_1 decreases. In this way, the conductions of the transistors 4 are controlled so as to produce the $V_1 = V_r$ condition, and, accordingly, current in accordance with the level of the reference voltage V_r flows through the respective transistors 4 into the LED's 3a-3c. Thus, the LED's 3a-3c are driven with constant current.

Switching transistors 9 are connected between the gates of the respective MOS transistors 4 and ground, respectively, for selectively driving the LED's 3a, 3b and 3c. The switching transistors 9 are turned on or off in response to a selection signal applied thereto from a control circuit 10, so that the desired LED's are selectively energized to emit light.

In the above-stated conventional type of LED driving integrated circuits, the resistors 5 and 8 are usually formed simultaneously as diffused resistors or polysilicon resistors, and, therefore, there is little variation in resistance value of the resistors in a particular one of the integrated circuits. However, depending on manufacturing conditions, more than $\pm 35\%$ of variations could be caused in resistance value among different integrated circuits, and accordingly, more than $\pm 35\%$ of variations in output current from such integrated circuits will be caused. That is, the output current of one integrated circuit could be two or more times that of other integrated circuits.

The quality of pictures produced by a printer depends on the uniformity of the light output of an LED heads. Therefore it is necessary to make the light output of the LED head as uniform as possible so that variations in light output may be, for example, within a range of less than $\pm 20\%$. Although not discussed herein, there are other major causes for variations of the light outputs than variations of integrated circuits, namely, variations of LED array chips and focusing lenses. Therefore, in order to

provide uniform light outputs, at least variations of driving power supplies of the integrated circuits must be reduced to, for example, less than $\pm 5\%$. In order to realize it, it is necessary to measure the output currents of respective integrated circuits and select those integrated circuits which have like values of the output currents. However, such a classification procedure is one of the largest causes of increase of the manufacturing costs.

The light output of an LED is temperature-dependent, and as temperature increases, the light output of an LED decreases. Accordingly, it is necessary that current to drive the LED be controlled in response to temperature. In the above-described conventional MOS integrated circuit 1 for driving LED's, the depletion-type MOS transistor 7 is used to develop the reference voltage V_r for temperature-controlling the drive current. However, the value of V_r varies from circuit to circuit due to varying manufacturing conditions which cannot be made constant, so the yield of usable integrated circuits is low, which raises the cost of the integrated circuits.

The present invention is to eliminate the above-stated disadvantages of conventional integrated circuits. According to the present invention, variations in values of output currents of integrated circuits can be minimized, so that no procedure of classifying manufactured integrated circuits is needed and the yield of usable integrated circuits increases. Thus, the manufacturing costs of LED array heads and, hence, ultimate products can be small.

According to a first embodiment of the present invention, an LED driving MOS integrated circuit includes a plurality of first constant current circuits, each for a respective one of a plurality of LED's to be driven. Each of the first constant current circuits comprises a current control element connected in series with an associated LED, a current detecting element connected in series with that LED for sensing current flowing into the LED and providing a detection voltage representing the sensed current, and a drive circuit which is responsive to a reference voltage and the detection voltage from the current detecting element to drive the current control element so as to maintain the drive current flowing into the LED at a predetermined value. The MOS integrated circuit according to the first embodiment of the present invention further includes a second constant current circuit common to all the LED's to be driven. The second constant current circuit is connected to the reference voltage input of the first constant current circuit and comprises, similar to the first constant current circuit, a current detecting element, a current control element and a driver circuit. The second constant circuit forms a mirror circuit with each of the first constant circuits.

According to a second embodiment of the

present invention, an LED driving MOS integrated circuit employs, in the second constant current circuit, a current detecting element which comprises a basic detecting component and a plurality of adjustment detecting components. A selection circuit enables desired ones of the adjustment detecting components to adjust the value of the current detecting element, as occasion demands.

Figure 1 is a circuit diagram of a conventional LED driving MOS integrated circuit;

Figure 2 is a circuit diagram of an LED driving MOS integrated circuit according to a first embodiment of the present invention;

Figure 3 is a circuit diagram of an LED driving MOS integrated circuit according to a second embodiment of the present invention; and

Figure 4 shows relationship between drive current and load resistance in the LED driving MOS integrated circuits of the present invention.

Now, the LED driving MOS integrated circuit according to the present invention is described in detail with reference to the accompanying drawings.

Figure 2 shows a basic circuit configuration of the LED driving MOS integrated circuit 11 of the present invention, which includes a plurality of current control elements 4a for respective ones of LED's 3 to be driven, and current control elements 4b and 4c. The current control elements 4a, 4b and 4c are MOS transistors of like characteristics. The integrated circuit 11 includes also a plurality of circuits 6a for driving respective ones of the current control elements 4a and a circuit 6b for driving the current control elements 4b and 4c. These circuits comprise voltage comparators of like configurations. There are also provided a plurality of current detecting elements 5a for the respective LED's 3 which are resistors in the form of diffused resistors or polysilicon resistors and have the same resistance value of R_{IN} . The integrated circuit further includes a load resistor 5b (having a resistance value of R_{IN}) like the resistors 5a, which acts as a voltage drop element for producing a first reference voltage, and a resistor 5c which has a resistance value R_E that is substantially equal to the resistance of the current detecting element resistors 5a. As will be stated later, the resistance of the resistor 5c is particularly precisely adjusted by means of trimming or like processing.

The MOS transistors 4a, the voltage comparators 6a and the resistors 5a correspond to the MOS transistors 4, the voltage comparators 6 and the resistors 5 in the conventional LED driving MOS integrated circuit shown in Figure 1, respectively, and form a plurality of first constant current circuits to which a voltage V_{r2} developed at a node B is applied as a first reference voltage. Each first con-

stant current circuit, which comprises one current control element 4a, one voltage comparator 6a and one resistor 5a, is associated with one of the LED's to be driven. One of the LED's 3 to be energized to emit light are selected by means of associated switches 9.

The two MOS transistors 4b and 4c, the voltage comparator 6b and the resistor 5c form a second constant current circuit which uses a voltage V_{r1} as a second reference voltage applied to a + input terminal of the voltage comparator 6b. The second constant current circuit forms mirror circuits with the respective first constant current circuits, whereby current I_0 flowing through the resistor 5c and current I_2 for driving each of the LED's 3 can be made equal. A voltage V_{DD} is applied to this integrated circuit from an external DC voltage source (not shown).

Now, the operation is explained. Since all of the first constant current circuits have the same configuration, the explanation of circuit operation is made in terms of only one of the first constant current circuit, but it should be noted that the same explanation is applicable to all the first constant current circuits.

In the second constant current circuit using, as its reference voltage, the voltage V_{r1} applied to the + input terminal of the voltage comparator 6b, the high precision resistor 5c having substantially the same resistance value as the resistors 5a and 5b is connected in series with the MOS transistor 4c. The current I_0 flows through the resistor 5c. The current I_0 is,

$$I_0 = (V_{DD} - V_{r1})/R_E$$

The voltage comparator 6b then develops an output voltage V_{G1} which is applied to the MOS transistor 4c as its gate voltage. This voltage V_{G1} is also applied, as a gate voltage, to the MOS transistor 4b which is formed to have the same characteristics as the transistor 4c, and, therefore, current I_1 flowing through the load resistor 5b has the same value as the current I_0 of the second constant current circuit. Accordingly, the reference voltage V_{r2} at the node B that is applied to the + input terminal of the voltage comparator 6a is expressed as follows.

$$V_{r2} = V_{DD} - R_{IN}(V_{DD} - V_{r1})/R_E$$

Then, the current I_2 flowing through the resistor 5a, which is the current detecting element in the first constant current circuit using V_{r2} as its reference voltage, is:

$$I_2 = (V_{DD} - V_{r2})/R_{IN} = (V_{DD} - V_{r1})/R_E = I_0$$

Thus, the drive current I_2 for the LED's 3 is determined by the high precision resistor 5c of the integrated circuit.

As stated previously, for a given designed value, the resistance value of the resistors 5a and 5b may vary by more than $\pm 35\%$ from one integrated

circuit to others, but within one particular integrated circuit, variations in resistance value of the resistors 5a and 5b are very small. Accordingly, in order to minimize variations in current value among different integrated circuits, it is sufficient to precisely adjust the values of the respective resistors 5c. In particular, by trimming. In stead of trimming the resistors 5, the resistors 5c may be provided outside the integrated circuits, since inexpensive resistors having variations in value of less than $\pm 1\%$ are commercially available, and can be used as the high-precision external resistors 5c. The use of one such external resistor for one integrated circuit can greatly reduce variations in current value among a number of integrated circuits. Also, it is possible to obtain any desired value for the drive current.

Figure 3 shows a second embodiment of the LED driving MOS integrated circuit according to the present invention. In Figure 3, the same reference numerals and symbols as used in Figure 2 are used for components and functions which appear also in Figure 2, and explanations about them are not made.

In the second embodiment shown in Figure 3, in addition to the resistor 5c, a plurality of resistance-adjustment resistors are used, for providing the load resistance for the transistor 4c. One or more of such resistance-adjustment resistors, as required, are connected in parallel with the resistor 5c. As shown in Figure 4, the LED driving current I_2 can be stably adjusted within a range of from 20% to 30% by adjusting the resistance value R_E of the load resistance for the transistor 4c.

In Figure 3, resistance-adjustment resistors 5d and 5e have resistance values that are, for example, about ten times that of the resistor 5c. Switching elements (transistors in this embodiment) 7d and 7e, when enabled, connect the resistors 5d and 5e in parallel with the resistor 5c, respectively. A selection circuit (latch circuit in this embodiment) 8 receives a latch signal LATCH to store data d_1 and d_2 for on-off controlling the respective switching elements 7d and 7e.

Thus, the load resistance for the transistor 4c can assume four values, namely, the resistance value when both of the switching elements 7d and 7e are conductive, the resistance value when both of them are nonconductive, the resistance value when only one of them is conductive, and the resistance value when only the other switching element is conductive. Thus, by means of the control data d_1 and d_2 , the load resistance value can be adjusted in four steps to thereby compensate for variations in value of the basic resistors 5c of different integrated circuits can be compensated for and, hence, variations of drive currents among the integrated circuits can be minimized. Furthermore, it is possible to set the drive

current at any desired value.

This second embodiment has been described to employ two resistance-adjustment resistors, but the number of the resistance-adjustment resistors can be increased to increase the adjustment steps accordingly so that the adjustment can be made more precisely.

In the first and second embodiments described in the above, it is necessary to control the LED drive current in response to temperature, because the light output of the LED's is dependent on temperature and decreases as temperature increases. This control can be provided by employing an arrangement in which the reference voltage V_{r1} is applied to the integrated circuit from an external source. Using this arrangement, it is also possible to make the drive current values of a plurality of integrated circuits equal to each other and, it is also possible to set any desired value for the drive currents.

As stated above, according to the present invention, the values of the drive currents in the LED driving MOS integrated circuits can readily be made equal to each other by highly precisely setting the load resistances of the second constant current circuits of the respective integrated circuits. Furthermore, by employing the arrangement to externally controlling the values of the drive currents, a uniform, stable light output can be provided from an LED array head driven by a plurality of integrated circuits of the present invention. In addition, if adjustable load resistors are used in the second constant current circuits, the same results as stated above can be obtained without incurring large expenses. Thus, according to the present invention, integrated circuits and, hence, LED array heads are fabricated at smaller costs.

Claims

1. An MOS integrated circuit for driving light-emitting diodes, comprising:
a plurality of first constant current circuits each for one of said light-emitting diodes, each of said first constant current circuits comprising a first current control element connected in series with one of said light-emitting diodes, a first current detecting element connected in series with said first current control element and that one of said light-emitting diodes, and a first drive circuit responsive to a detection voltage from said first current detecting element and to a first reference voltage, for driving said first current control element in such a manner that the current flowing through that light-emitting diode is maintained at a fixed value;
a voltage-drop element for providing said first reference voltage; and

a second constant current circuit connected to form a mirror circuit with each of said plurality of first constant current circuits, said second constant current circuit comprising a second current control element connected in series with said voltage-drop element for controlling current flowing into said voltage-drop element from a DC voltage source, and a second drive circuit responsive to a detection voltage from a second current detecting element connected to a DC voltage source so as to conduct a predetermined magnitude of current therethrough and also responsive to a second reference voltage, for driving said second current control element in such a manner that current flowing through said voltage-drop element is maintained at a fixed value.

2. The MOS integrated circuit for driving light-emitting diodes, according to Claim 1, wherein said second current detecting element comprises a basic component and a plurality of adjustment components which can be connected in parallel with said basic component as occasion demands, the adjustment components to be connected to said basic element being selected by a selection circuit.

3. The MOS integrated circuit for driving light-emitting diodes, according to Claim 1, wherein said second current detecting element has its value precisely set and is fabricated in an integrated circuit form with the other circuit elements.

4. The MOS integrated circuit for driving light-emitting diodes, according to Claim 1, wherein said second current detecting element is disposed external to the integrated circuit.

5. The MOS integrated circuit for driving light-emitting diodes, according to any one of Claims 1 to 4, wherein said second reference voltage is externally applied to the integrated circuit.

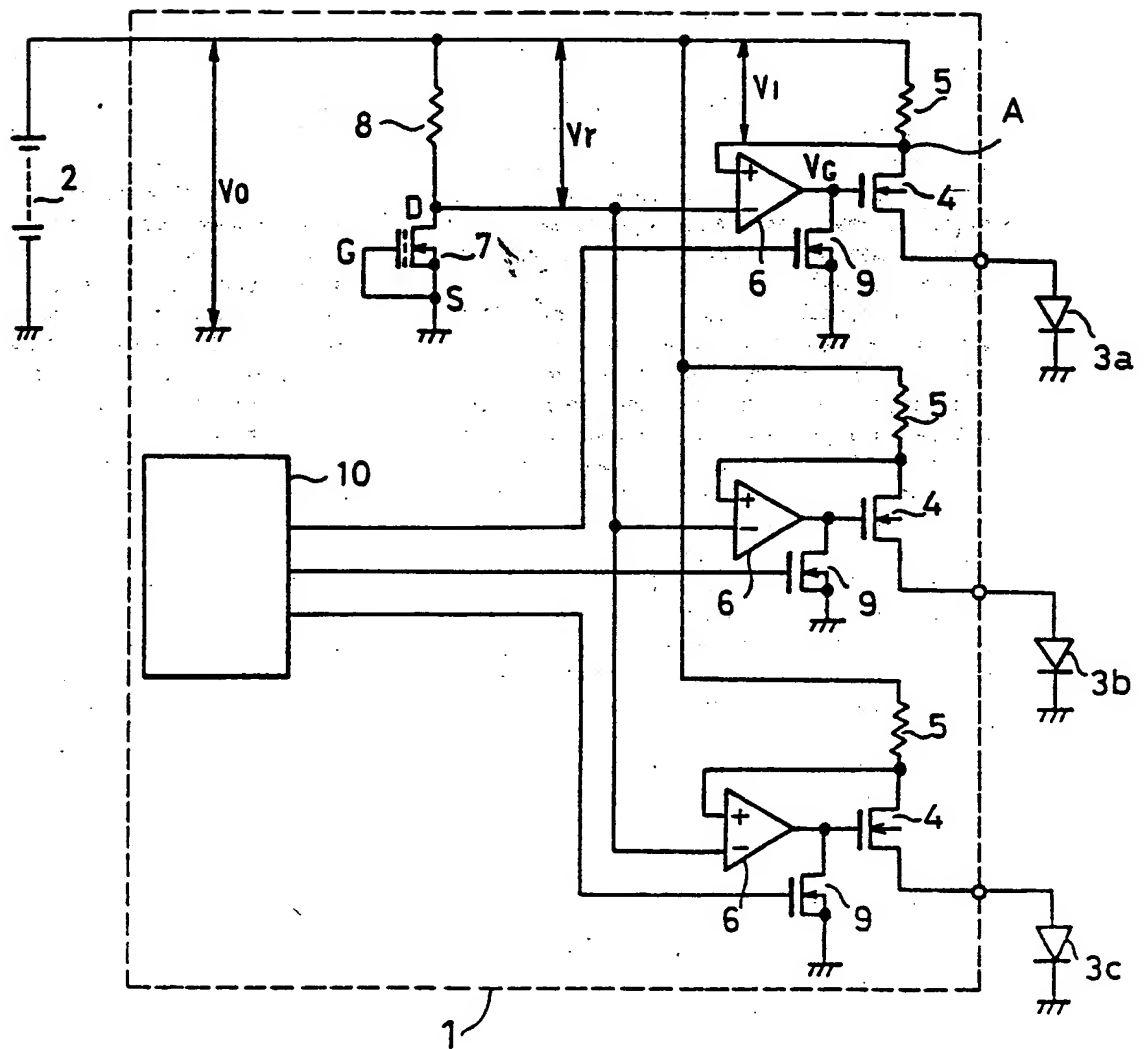


FIG. 1

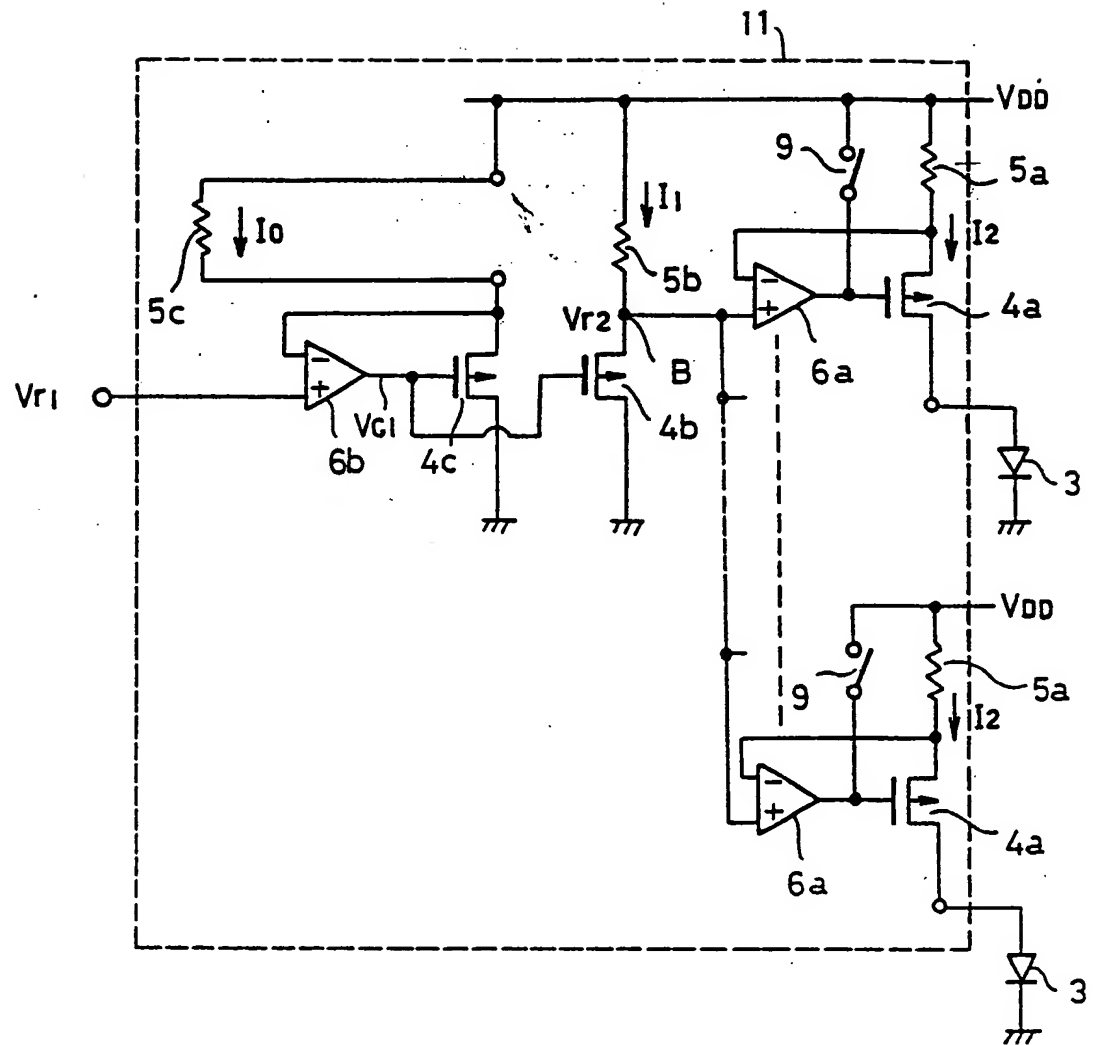


FIG. 2

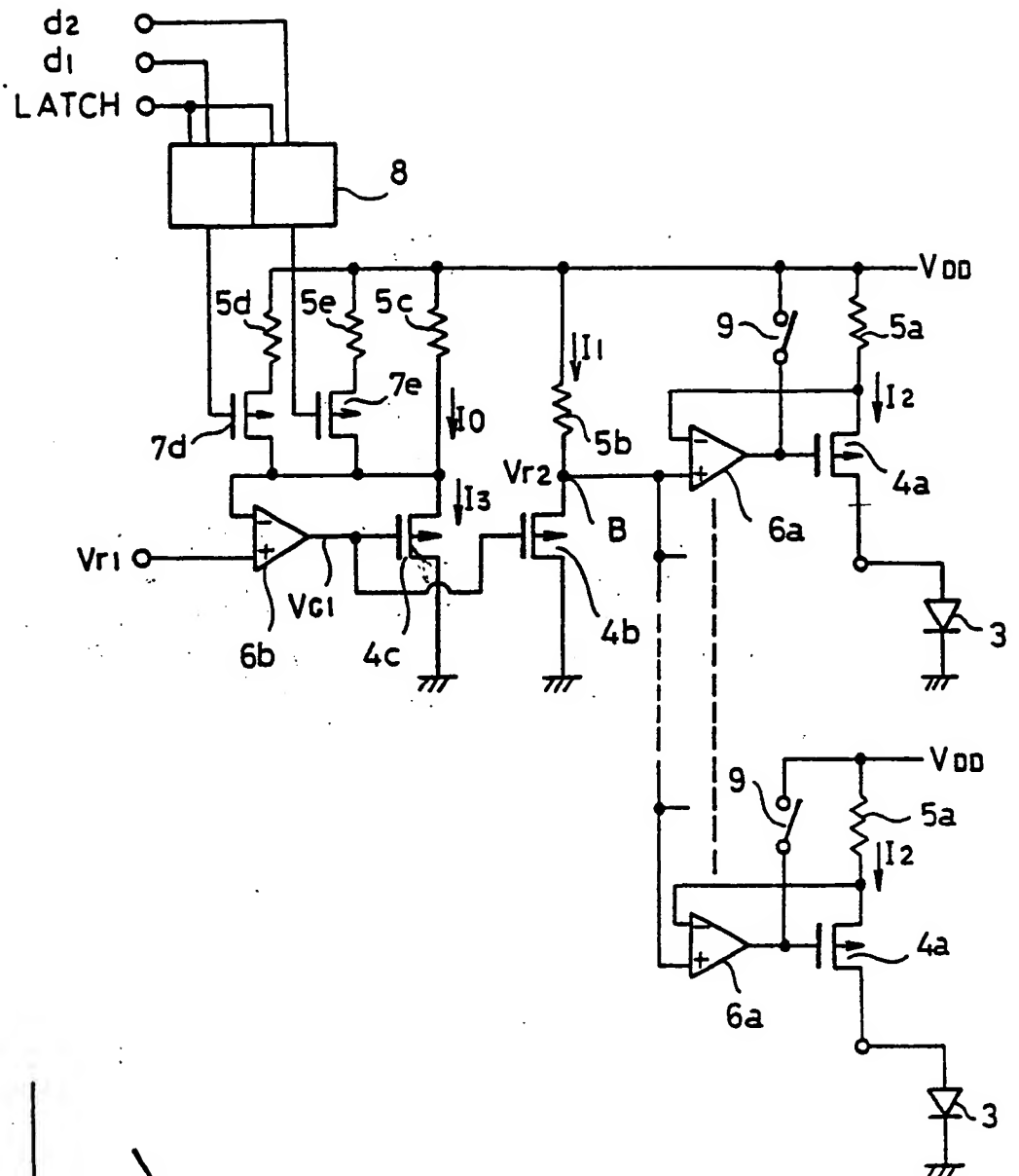


FIG. 3

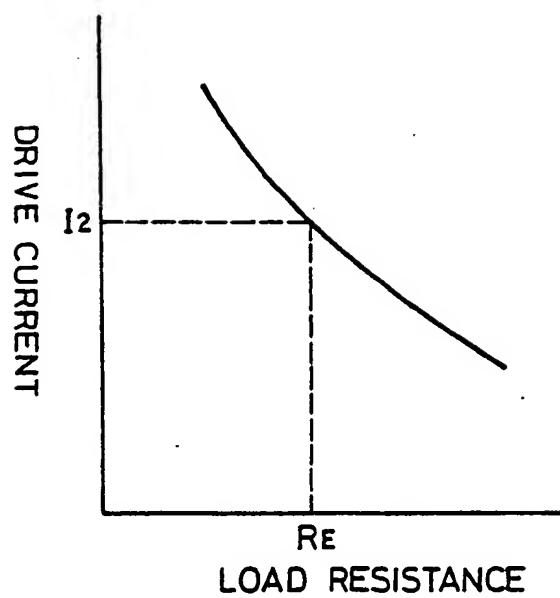


FIG. 4



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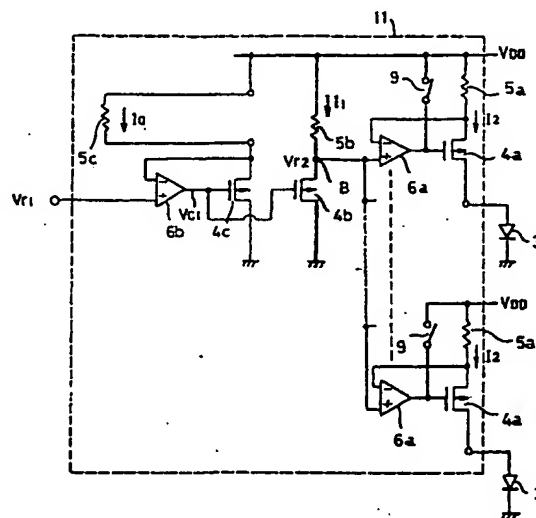


FIG. 2

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EUROPEAN SEARCH REPORT

Application Number

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	US-A-4 739 246 (THOMSON) * Column 1, lines 26-43 *	1	G 06 K 15/12 H 01 L 33/00
A	P.R. GRAY et al.: "Analysis and design of analog integrated circuits", 2nd edition, 1984, pages 703-764, chapter 12, John Wiley and Sons * Page 736, line 1 - page 737, line 4; figure 12.29 *		
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			G 06 K G 05 F H 05 B
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of search 03 October 91	Examiner VEEN G.E.
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